FIG. 1
BACKGROUND ART

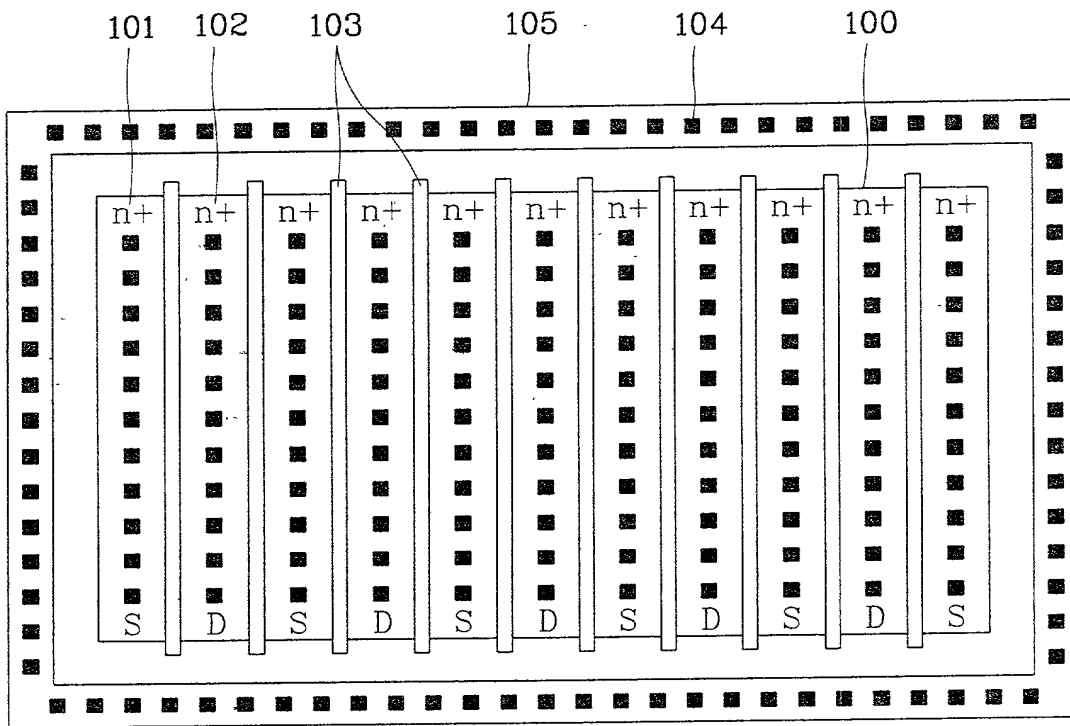# FIG. 2
BACKGROUND ART
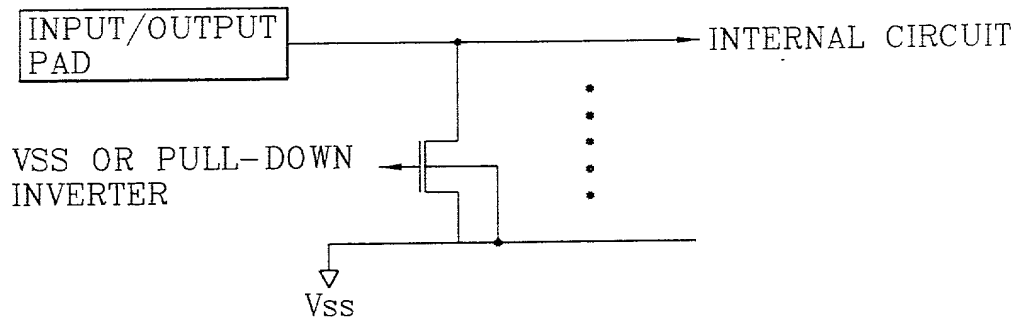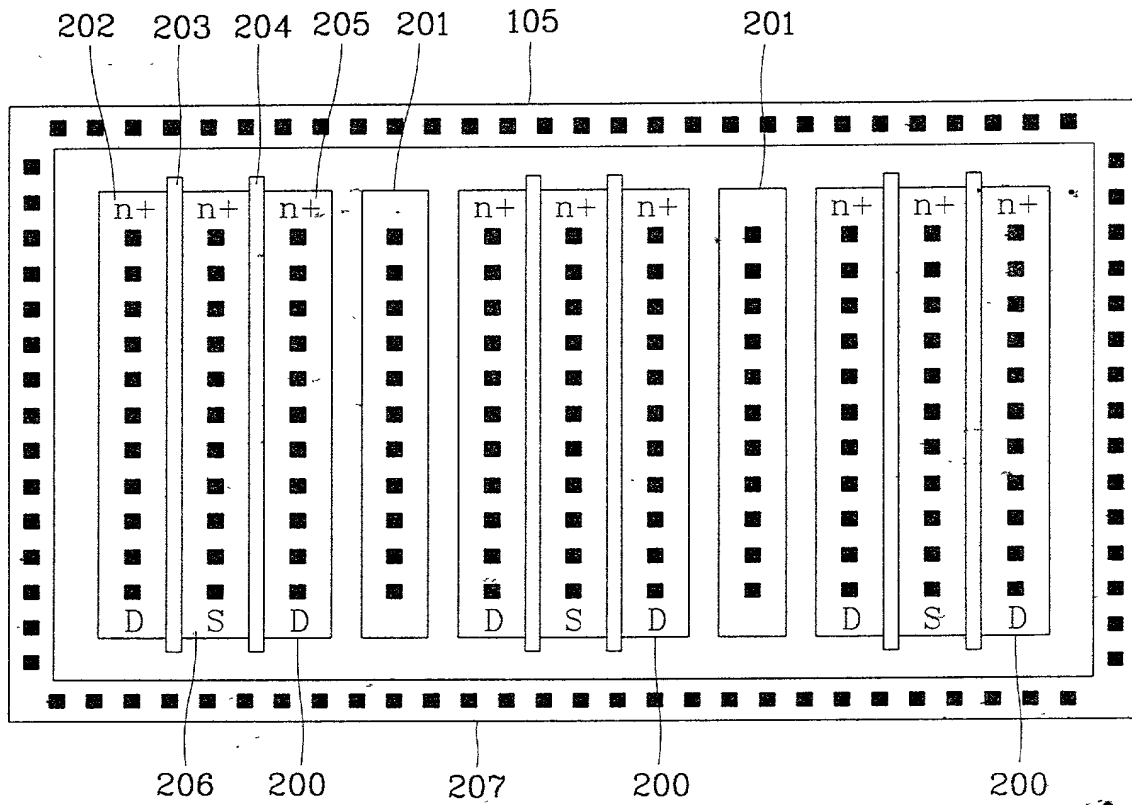
FIG. 3

202 203 204 205 201   105   201

n+ n+ n+ .   n+ n+ n+   n+ n+ n+

D   S   D   D   S   D   D   S   D

206 200   207   200   200

FIG. 4

Vcc

INPUT/OUTPUT
PAD   INTERNAL CIRCUIT

VSS OR PULL-DOWN
INVERTER

Vss